

FIG. 1

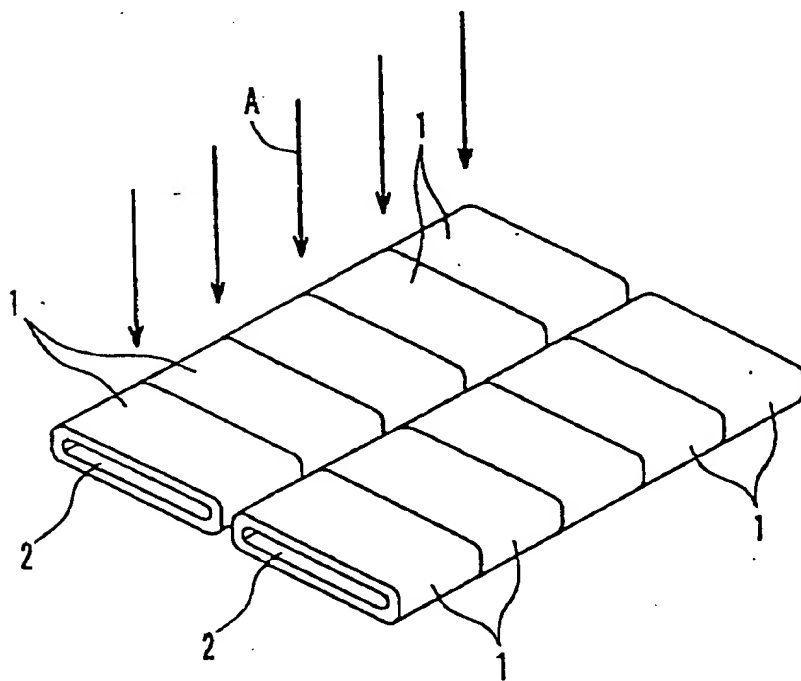
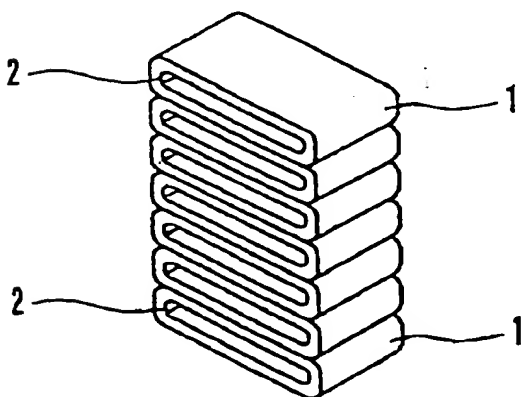


FIG. 2



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FIG. 3

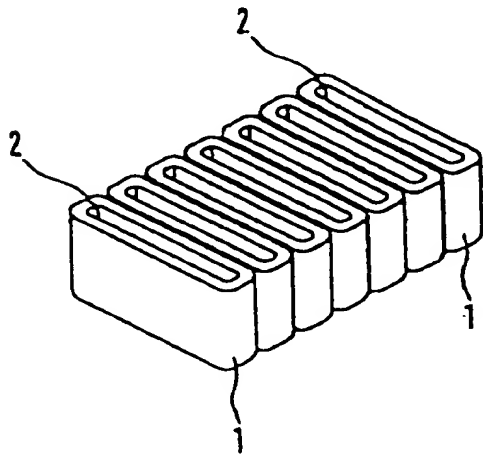


FIG. 4

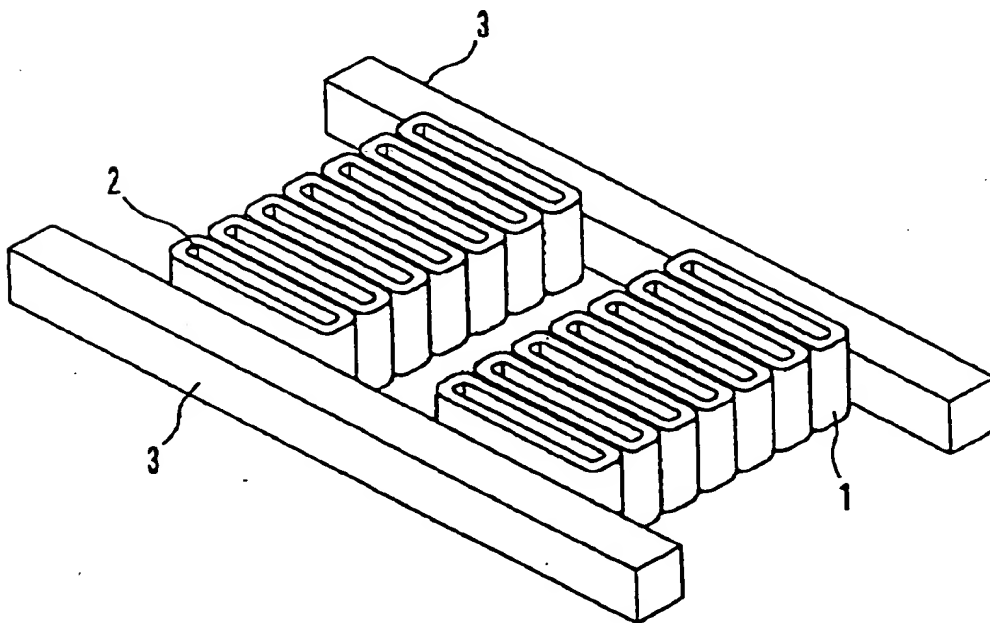




FIG. 6

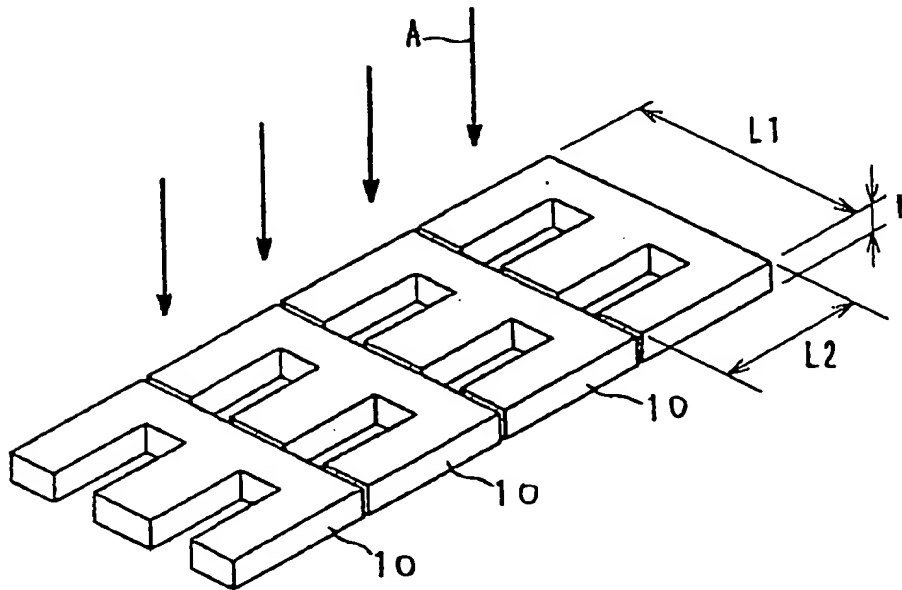
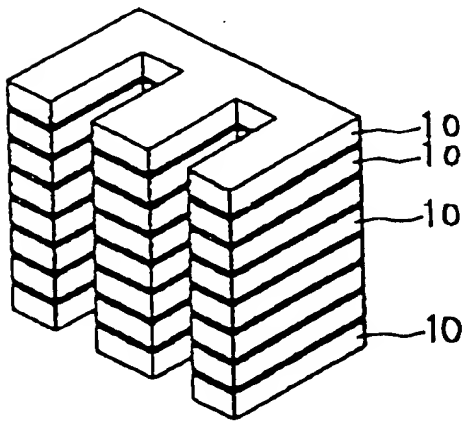


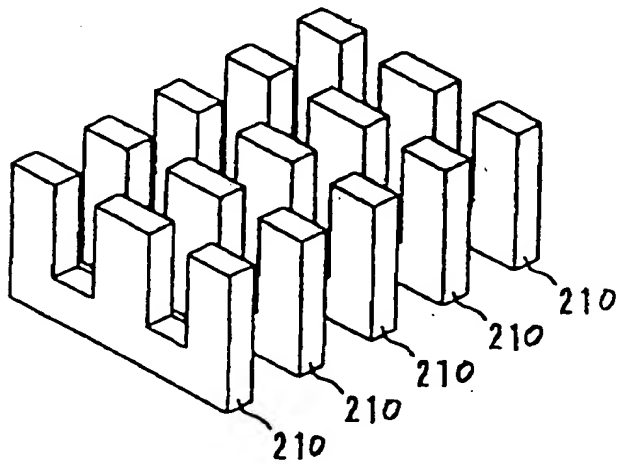
FIG. 7



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Figure 1 is a perspective view of a first embodiment of a semiconductor device. It shows a substrate 30 with a grid of rectangular blocks 10 arranged in a staggered pattern on its surface.

FIG. 10



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